

Docket No.: P2001,0325

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : HOLGER SEDLAK ET AL.

Filed : CONCURRENTLY HEREWITH

Title : FREQUENCY REGULATING CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,974,557 (Thomas et al.), dated October 26, 1999;

U.S. Patent No. 5,465,063 (Fukuda et al.), dated November 7, 1995, and corresponding German Published Non-Prosecuted Patent Application DE 43 14 321 A1 (Fukuda et al.), dated February 10, 1994;

German Patent DE 41 14 363 C2 (Rabi), dated December 12, 1991, and English abstract thereof;

European Patent Specification EP 0 477 907 B1 (Suwada et al.), dated April 1, 1992, and German Translation DE 691 18 798 T2 (Suwada et al.), dated April 1, 1992;

European Patent Application EP 1 061 649 A1 (Flock et al.), dated December 20, 2000, and English abstract thereof;

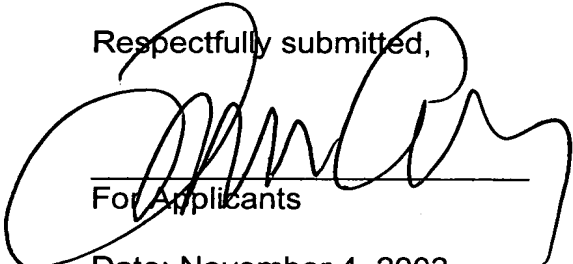
Patent Abstracts of Japan 10240373 (Yasushi), dated September 11, 1998;

Moore, B. D.: "Tradeoffs in Selecting IC Temperature Sensors", Microprocessors and Microsystems, Elsevier Science B. V., Vol. 23, June 3, 1999, 181-184;

International Search Report, dated May 7, 2003.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant. As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,



For Applicants

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Date: November 4, 2003

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: P2001,0325 Appl. No.: <hr/> Applicant: HOLGER SEDLAK ET AL. <hr/> Filing Date: November 4, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	5,974,557	10/26/99	Thomas et al.			
	B	5,465,063	11/7/95	Fukuda et al.			
	C						
	D						
	E						
	F						
	G						
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	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	43 14 321 A1	2/10/94	Germany			
	K	41 14 363 C2	12/12/91	Germany			
	L	691 18 798 T2	4/1/92	Germany			
	M	0 477 907 B1	4/1/92	Europe			
	N	1 061 649 A1	12/20/00	Europe			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
EXAMINER				DATE CONSIDERED			

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EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
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	G						
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FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	10240373	9/11/98	Japan			
	K						
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Moore, B. D.: "Tradeoffs in Selecting IC Temperature Sensors", Microprocessors and Microsystems, Elsevier Science B. V., Vol. 23, June 3, 1999, 181-184					
EXAMINER				DATE CONSIDERED			